

FIG. 1

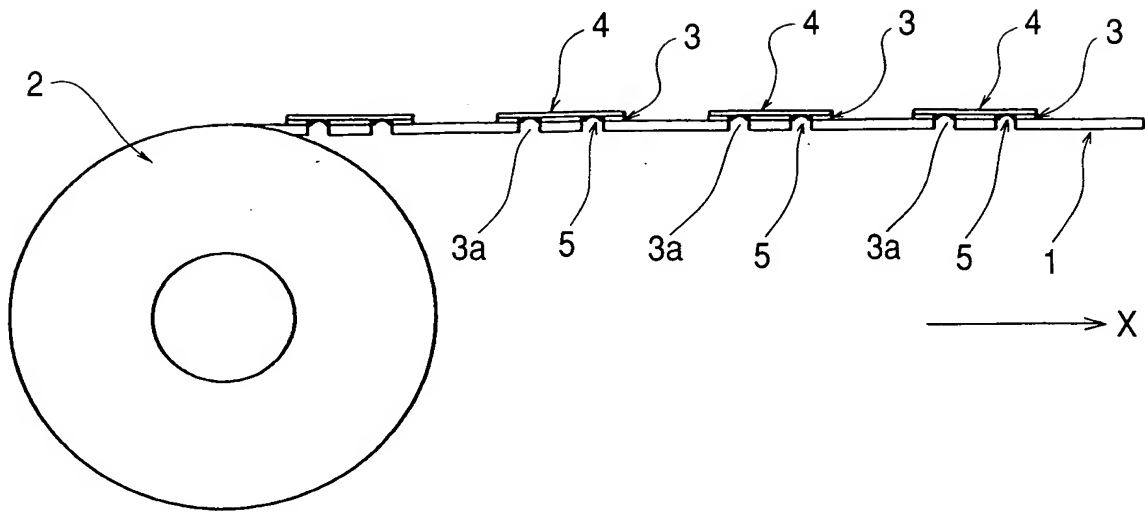


FIG. 2

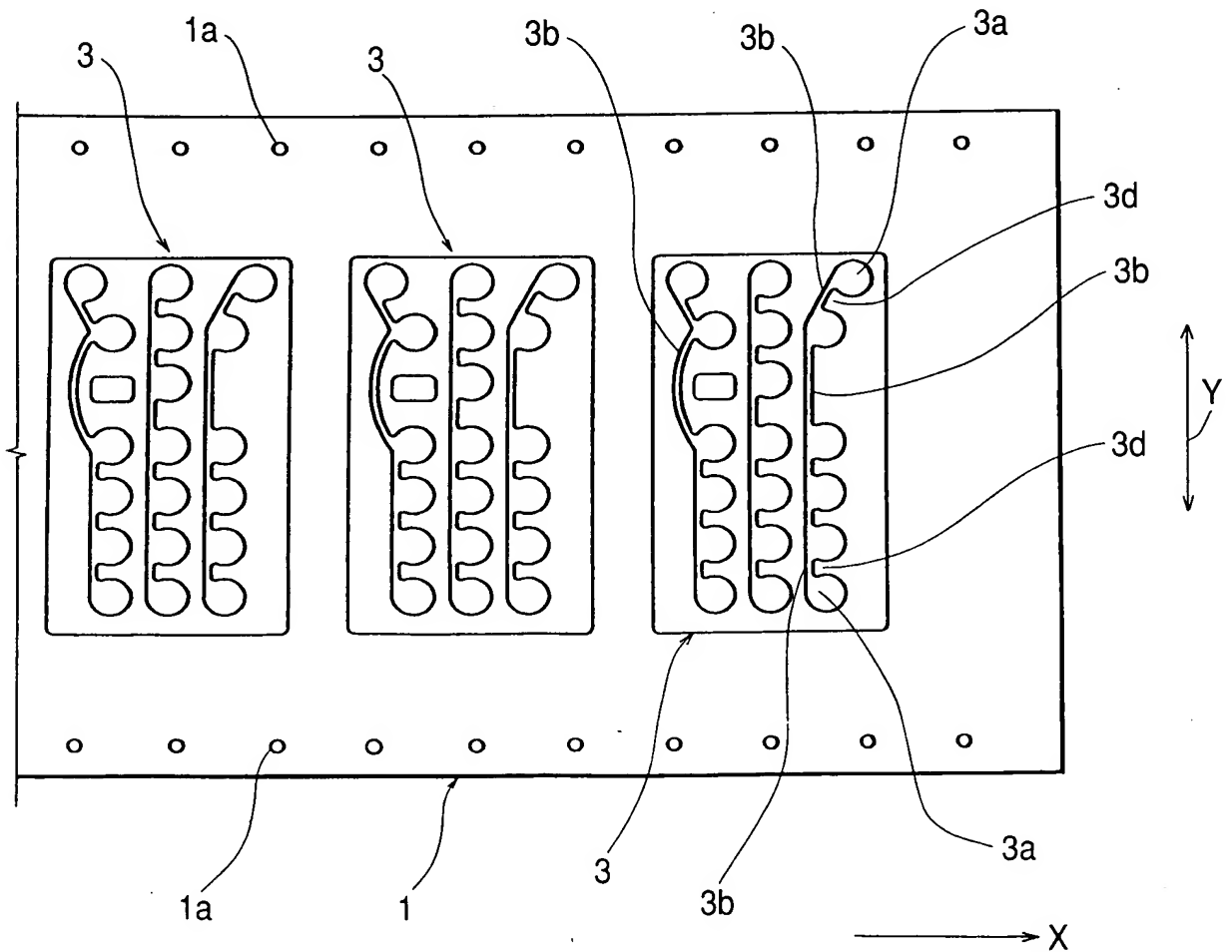


Figure 1 is a schematic diagram of a semiconductor device 1. The device 1 is shown as a rectangular substrate. A central rectangular region 3 is defined within the substrate. This central region 3 contains a grid of circular elements 3a, which are arranged in a pattern that is symmetric about a central vertical axis 3d. The central region 3 is surrounded by a border 3c. The entire device 1 is surrounded by a border 1a. A coordinate system with X and Y axes is shown on the right side of the diagram.

FIG. 4

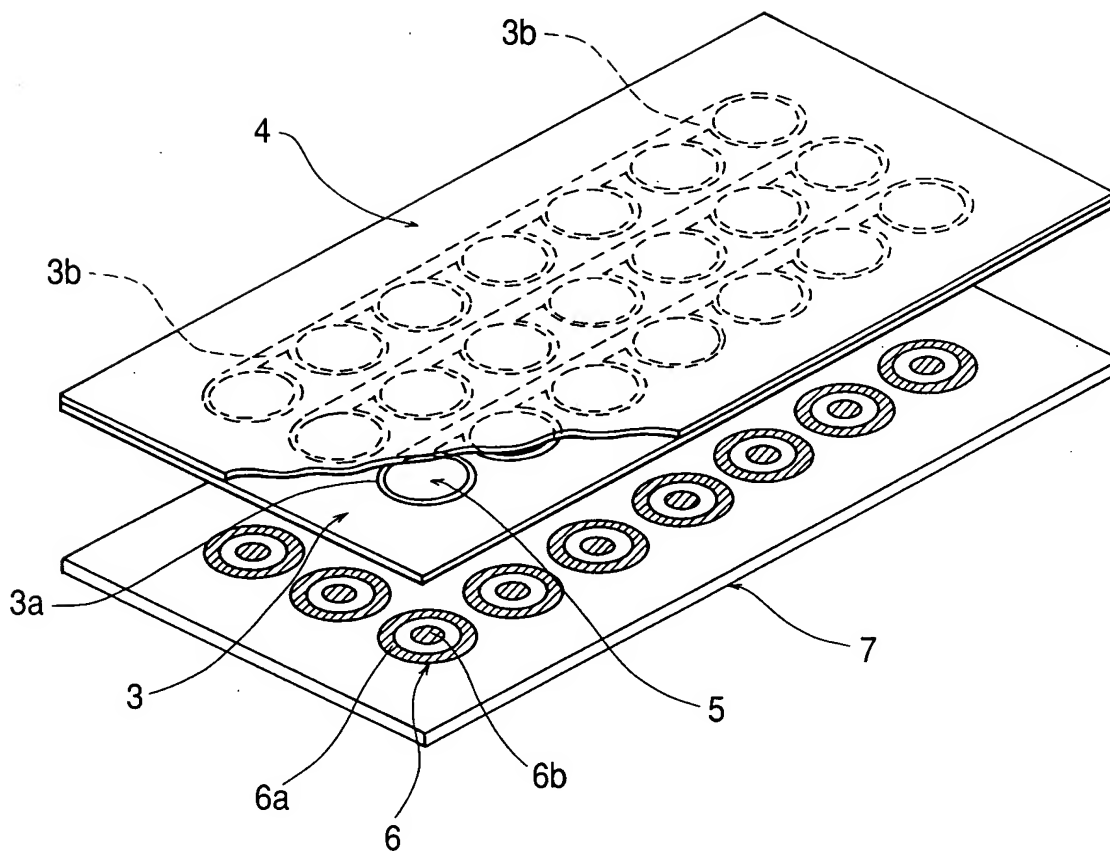


FIG. 5

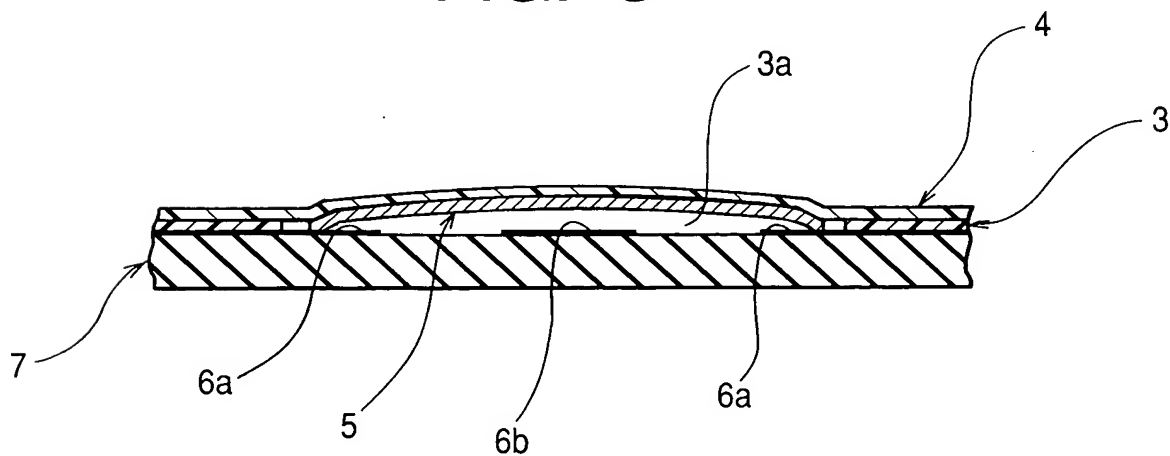


FIG. 6
PRIOR ART

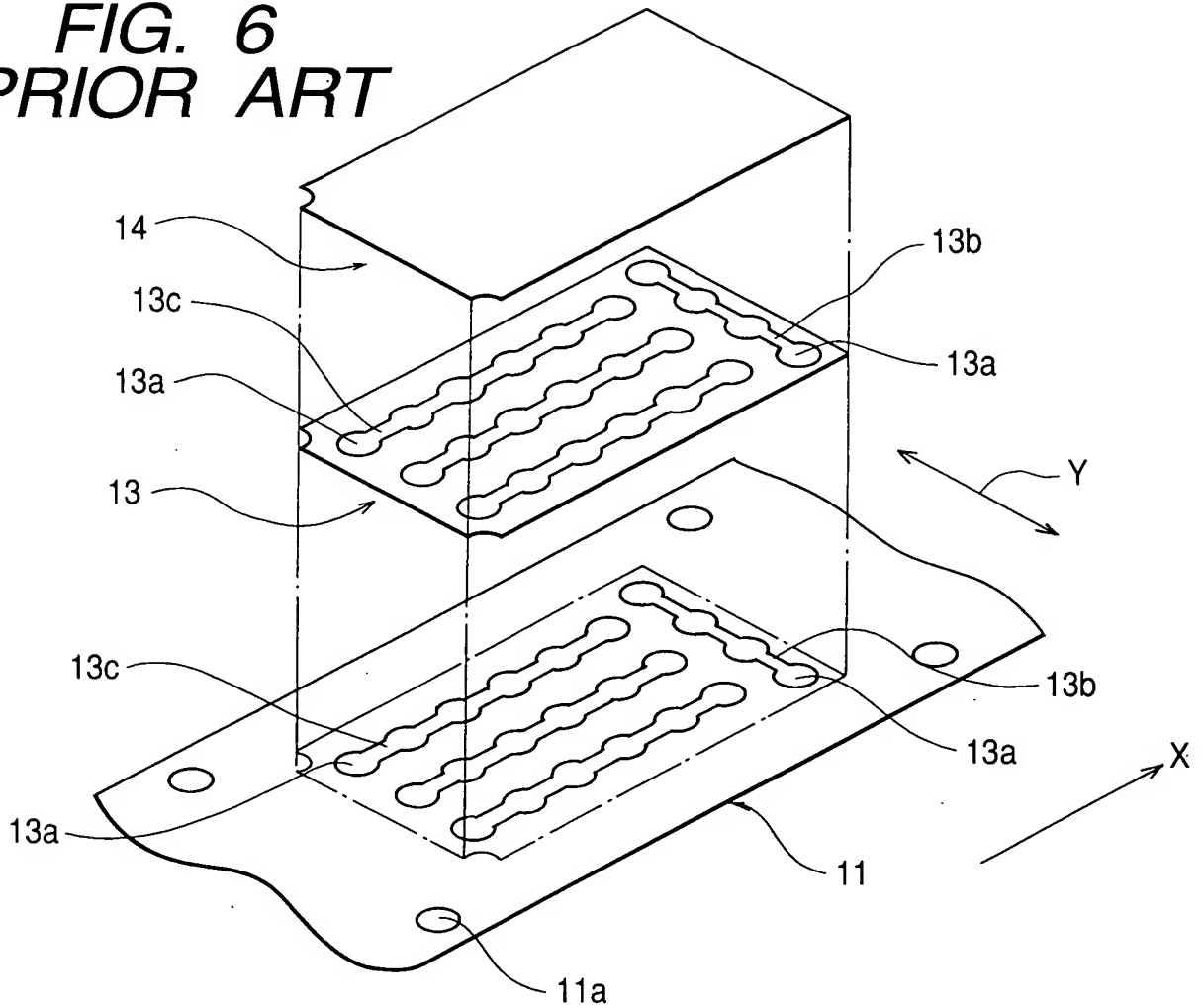


FIG. 7
PRIOR ART

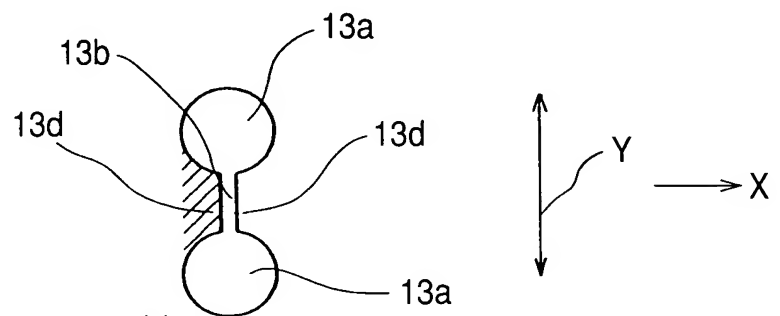


FIG. 8
PRIOR ART

